

Frequency Synthesizer

KSN-2065A-119+

50Ω 1989 to 2065 MHz

The Big Deal

- Low phase noise and spurious
- Robust design and construction
- Small size 0.80" x 0.58" x 0.15"



CASE STYLE: DK1042

Product Overview

The KSN-2065A-119+ is a Frequency Synthesizer, designed to operate from 1989 to 2065 MHz for W-CDMA application. The KSN-2065A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: <ul style="list-style-type: none">• Phase Noise: -107 dBc/Hz typ. @ 10 kHz offset• Comparison Spurious: -100 dBc typ.• Reference Spurious: -118 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of KSN-2065A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Small size, 0.80" x 0.58" x 0.15"	The small size enables the KSN-2065A-119+ to be used in compact designs.



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50Ω 1989 to 2065 MHz

Features

- Integrated VCO + PLL
- Low phase noise and spurious
- Robust design and construction
- Low operating voltage (VCC VCO=+5V, VCC PLL=+3.3V)
- Small size 0.80" x 0.58" x 0.15"

Applications

- W-CDMA

General Description

The KSN-2065A-119+ is a Frequency Synthesizer, designed to operate from 1989 to 2065 MHz for W-CDMA application. The KSN-2065A-119+ is packaged in a metal case (size of 0.80" x 0.58" x 0.15") to shield against unwanted signals and noise. To enhance the robustness of KSN-2065A-119+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

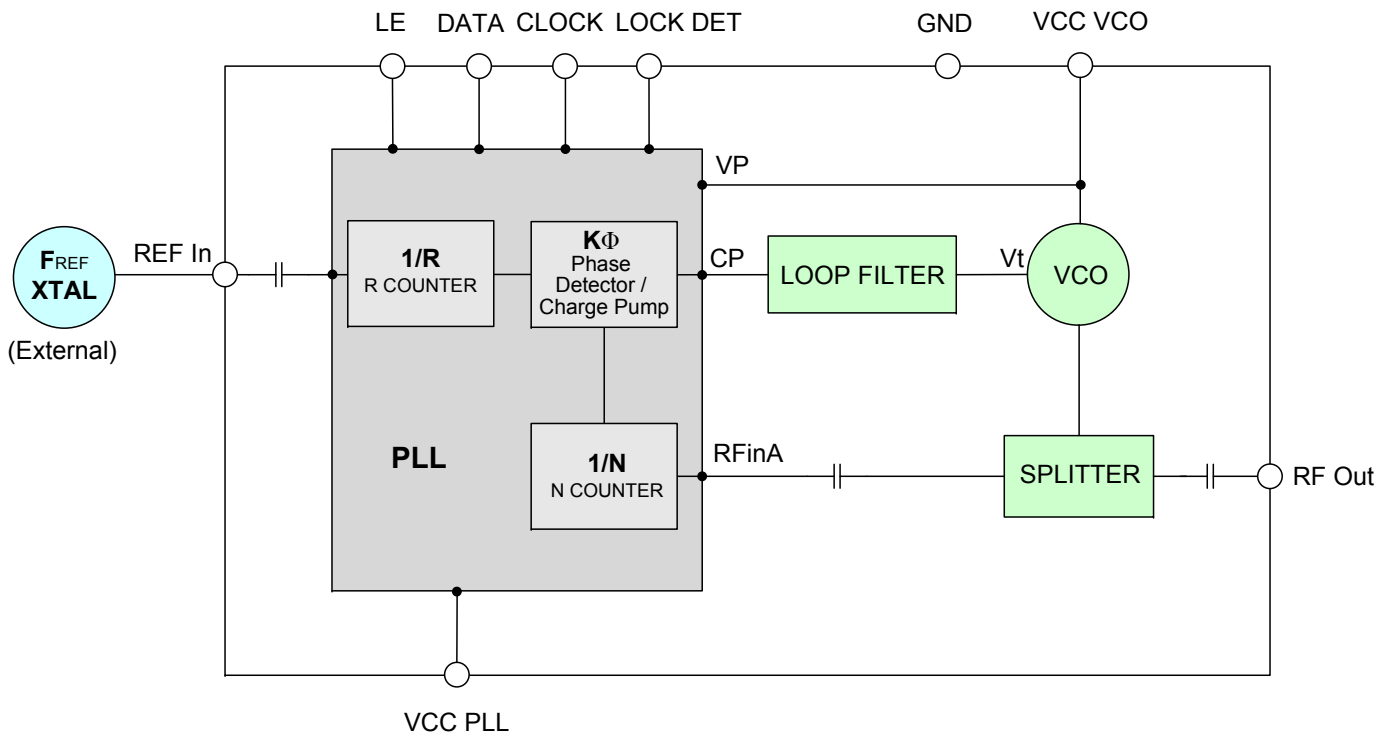


CASE STYLE: DK1042
 PRICE: \$29.95 ea. QTY (1-9)

+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

Simplified Schematic



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REV. OR
 M126018
 EDR-8798F1
 KSN-2065A-119+
 Category-A1
 RAV
 100321
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Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters	Test Conditions	Min.	Typ.	Max.	Units
Frequency Range	-	1989	-	2065	MHz
Step Size	-	-	200	-	kHz
Settling Time	Within ± 1 kHz	-	40	-	mSec
Output Power	-	+1.0	+3.5	+6.0	dBm
SSB Phase Noise	@ 100 Hz offset	-	-64	-	dBc/Hz
	@ 1 kHz offset	-	-76	-70	
	@ 10 kHz offset	-	-107	-98	
	@ 100 kHz offset	-	-128	-120	
	@ 1 MHz offset	-	-149	-140	
Reference Spurious Suppression	Ref. Freq. 26 MHz	-	-118	-90	dBc
Comparison Spurious Suppression	Step Size 200 kHz	-	-100	-75	
Non - Harmonic Spurious Suppression	-	-	-90	-	
Harmonic Suppression	-	-	-27	-20	
VCO Supply Voltage	+5.00	+4.75	+5.00	+5.25	
PLL Supply Voltage	+3.30	+3.15	+3.30	+3.45	V
VCO Supply Current	-	-	47	54	mA
PLL Supply Current	-	-	7	14	
Reference Input (External)	Frequency	26 (square wave)	-	26	MHz
	Amplitude	1	-	1	V _{P-P}
	Input impedance	-	-	100	KΩ
	Phase Noise @ 1 kHz offset	-	-	-135	dBc/Hz
RF Output port Impedance	-	-	50	-	Ω
Input Logic Level	Input high voltage	-	2.80	-	V
	Input low voltage	-	-	0.60	V
Digital Lock Detect	Locked	-	2.75	-	V
	Unlocked	-	-	0.40	V
Frequency Synthesizer PLL	-	ADF4118			
PLL Programming	-	3-wire serial 3.3V CMOS			
Register Map @ 2065 MHz	F_Register	-	(MSB) X0XXX00000X0010010010 (LSB)		
	N_Register	-	(MSB) 100001010000101010101 (LSB)		
	R_Register	-	(MSB) 1XXXX0000001000001000 (LSB)		

Absolute Maximum Ratings

Parameters	Ratings
VCO Supply Voltage	6V
PLL Supply Voltage	6V
VCO Supply Voltage to PLL Supply Voltage	-0.3V to +5.5V
Reference Frequency Voltage	-0.3Vmin, VCC PLL +0.3Vmax
Data, Clock, LE Levels	-0.3Vmin, VCC PLL +0.3Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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Typical Performance Data

FREQUENCY (MHz)	POWER OUTPUT (dBm)			VCO CURRENT (mA)			PLL CURENT (mA)		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
	1989	3.25	3.57	3.51	44.55	46.74	47.89	5.98	7.61
1995	3.23	3.57	3.50	44.53	46.72	47.87	5.98	7.61	8.84
2005	3.09	3.47	3.40	44.51	46.68	47.83	5.97	7.60	8.84
2015	2.96	3.31	3.24	44.49	46.65	47.80	5.99	7.63	8.87
2025	2.94	3.25	3.16	44.48	46.63	47.77	5.98	7.61	8.86
2035	3.03	3.29	3.19	44.46	46.62	47.74	6.00	7.63	8.88
2045	3.15	3.39	3.28	44.45	46.60	47.71	5.99	7.62	8.87
2055	3.30	3.52	3.40	44.44	46.58	47.67	5.98	7.61	8.86
2065	3.39	3.63	3.50	44.42	46.56	47.65	6.00	7.63	8.88

FREQUENCY (MHz)	HARMONICS (dBc)					
	F2			F3		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
1989	-38.14	-39.42	-35.78	-25.97	-27.64	-30.02
1995	-38.26	-39.28	-35.82	-26.37	-27.64	-30.15
2005	-37.64	-38.53	-35.33	-26.29	-27.68	-30.28
2015	-36.73	-37.60	-34.41	-26.41	-27.97	-30.23
2025	-36.49	-37.47	-34.52	-25.39	-27.21	-29.90
2035	-35.77	-37.21	-34.57	-25.36	-26.49	-28.94
2045	-35.11	-36.76	-34.29	-25.27	-26.73	-29.42
2055	-35.15	-36.81	-34.59	-24.39	-25.87	-28.44
2065	-34.92	-36.90	-35.01	-24.76	-25.90	-28.77



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FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @ OFFSETS				
	+25°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1989	-64.00	-76.96	-106.81	-128.93	-149.18
1995	-62.99	-77.61	-107.04	-128.78	-149.12
2005	-62.91	-77.38	-106.65	-128.73	-149.08
2015	-62.22	-75.55	-106.49	-128.46	-148.74
2025	-64.93	-76.22	-105.90	-128.55	-148.82
2035	-65.29	-76.04	-105.74	-128.48	-148.31
2045	-64.63	-74.59	-106.06	-128.16	-147.98
2055	-62.42	-74.53	-106.14	-128.02	-148.03
2065	-62.07	-75.88	-105.60	-127.74	-147.69

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @ OFFSETS				
	-45°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1989	-66.40	-76.45	-106.86	-129.81	-150.09
1995	-67.20	-76.22	-106.75	-129.60	-149.85
2005	-63.84	-75.66	-106.55	-129.61	-150.00
2015	-64.54	-75.51	-106.62	-129.47	-150.15
2025	-65.22	-75.99	-106.50	-129.62	-149.84
2035	-65.66	-75.49	-106.20	-129.37	-149.92
2045	-63.76	-75.36	-106.18	-129.18	-150.04
2055	-64.50	-76.25	-106.02	-129.15	-149.60
2065	-63.31	-75.78	-105.85	-129.12	-149.60

FREQUENCY (MHz)	PHASE NOISE (dBc/Hz) @ OFFSETS				
	+85°C				
	100Hz	1kHz	10kHz	100kHz	1MHz
1989	-61.46	-77.11	-105.46	-127.75	-147.59
1995	-63.33	-76.08	-105.46	-127.46	-147.88
2005	-61.76	-77.20	-104.96	-127.30	-147.48
2015	-63.56	-76.07	-105.01	-127.18	-147.46
2025	-68.89	-75.78	-104.77	-126.99	-147.33
2035	-64.96	-75.73	-104.39	-126.78	-146.90
2045	-64.28	-75.87	-104.28	-126.51	-146.44
2055	-63.83	-76.15	-103.58	-126.32	-146.34
2065	-60.43	-74.28	-103.65	-125.90	-146.06



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COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @ Fcarrier 1989MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @ Fcarrier 2027MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @ Fcarrier 2065MHz+(n*Fcomparison) (dBc) note 1			
	n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
	-5	-124.84	-120.85	-123.10	-112.99	-109.57	-115.90	-128.35	-117.89	-123.56
-4	-117.64	-118.37	-119.79	-125.15	-123.72	-114.29	-128.13	-114.60	-121.28	
-3	-113.64	-115.45	-119.10	-118.98	-122.40	-111.51	-119.13	-114.34	-119.45	
-2	-105.89	-107.91	-110.27	-109.61	-116.14	-109.75	-112.49	-107.85	-118.06	
-1	-89.01	-94.73	-98.02	-94.86	-101.94	-98.89	-104.82	-97.49	-102.70	
0 ^{note 2}	-	-	-	-	-	-	-	-	-	
+1	-91.28	-98.46	-100.10	-97.54	-102.54	-99.68	-103.15	-100.98	-102.25	
+2	-108.68	-110.13	-113.65	-115.82	-115.92	-108.39	-111.25	-111.56	-115.83	
+3	-118.00	-114.12	-117.05	-123.06	-117.00	-111.86	-116.85	-112.15	-116.39	
+4	-123.94	-117.42	-123.72	-126.85	-118.38	-112.89	-118.21	-117.26	-121.93	
+5	-123.89	-119.10	-122.01	-114.44	-109.20	-113.39	-118.99	-116.11	-123.65	

Note 1: Comparison frequency 200 kHz
 Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @ Fcarrier 1989MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @ Fcarrier 2027MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @ Fcarrier 2065MHz+(n*Freference) (dBc) note 3			
	n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
	-5	-115.33	-127.01	-125.08	-121.56	-126.79	-126.39	-124.30	-129.54	-128.99
-4	-119.47	-132.77	-126.95	-120.68	-122.79	-123.21	-122.23	-125.42	-125.45	
-3	-123.74	-128.82	-129.60	-129.17	-130.52	-134.09	-127.36	-131.42	-132.50	
-2	-118.48	-122.00	-118.93	-117.90	-117.43	-117.68	-117.30	-118.57	-117.24	
-1	-126.28	-118.47	-115.06	-123.50	-119.45	-113.01	-120.90	-116.38	-111.22	
0 ^{note 4}	-	-	-	-	-	-	-	-	-	
+1	-115.95	-111.68	-112.73	-113.73	-110.32	-111.50	-112.85	-112.41	-111.41	
+2	-124.15	-122.17	-122.51	-122.51	-122.14	-120.45	-120.28	-121.72	-121.57	
+3	-124.01	-132.51	-134.87	-130.48	-136.24	-137.77	-133.25	-141.24	-130.41	
+4	-123.88	-126.70	-127.33	-123.12	-127.29	-126.40	-126.06	-131.19	-128.06	
+5	-116.21	-126.22	-133.18	-121.97	-129.14	-136.33	-123.72	-127.30	-132.92	

Note 3: Reference frequency 26 MHz
 Note 4: All spurs are referenced to carrier signal (n=0).



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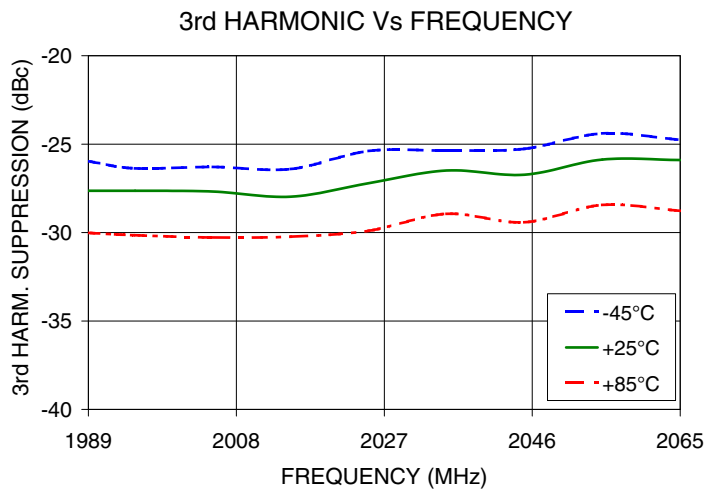
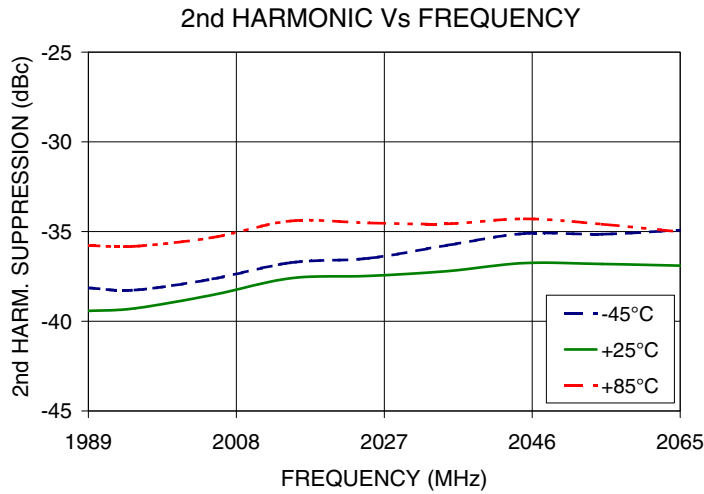
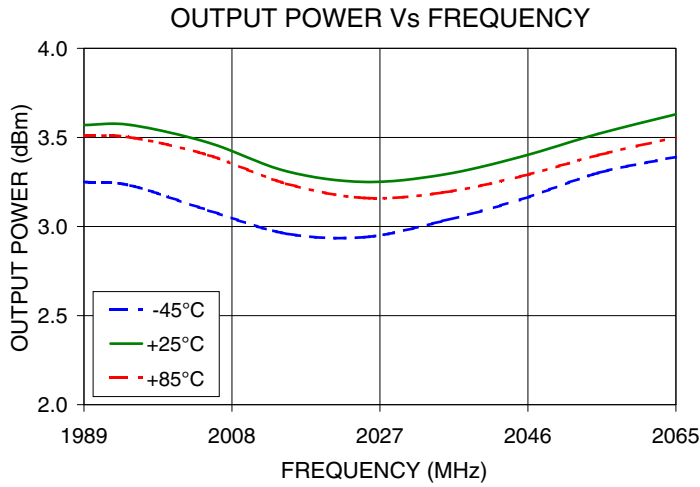


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Typical Performance Curves



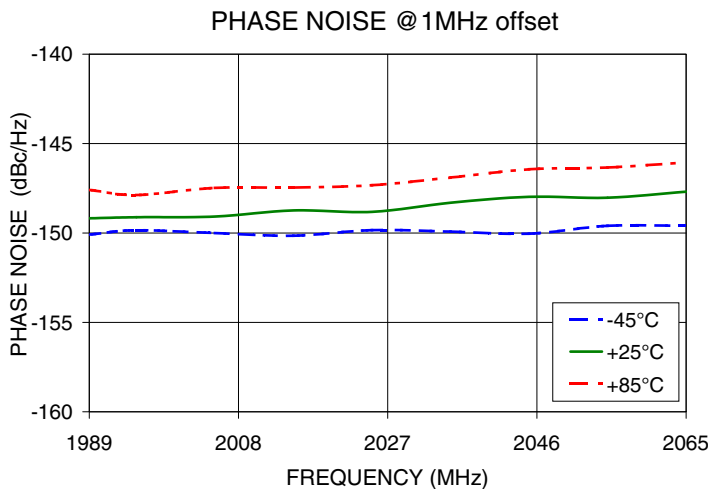
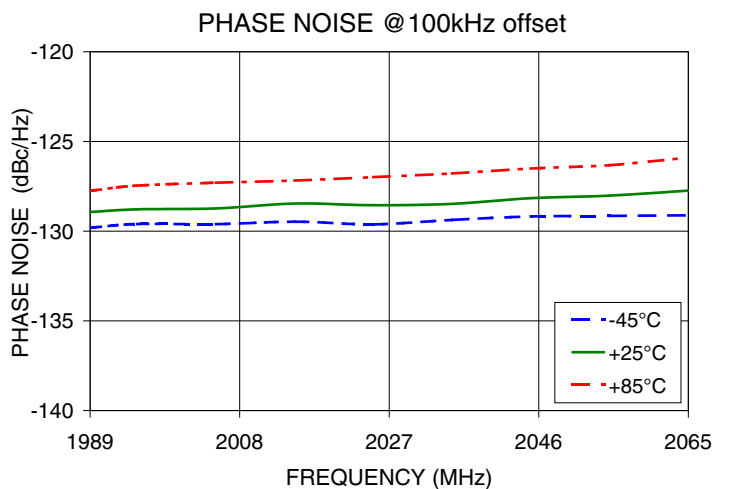
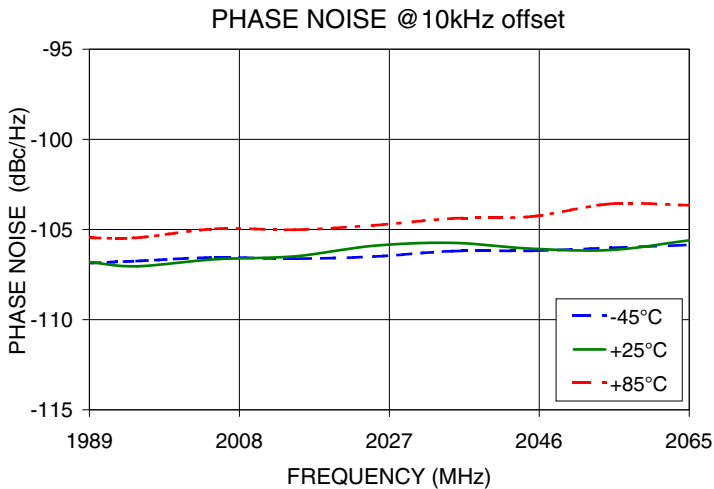
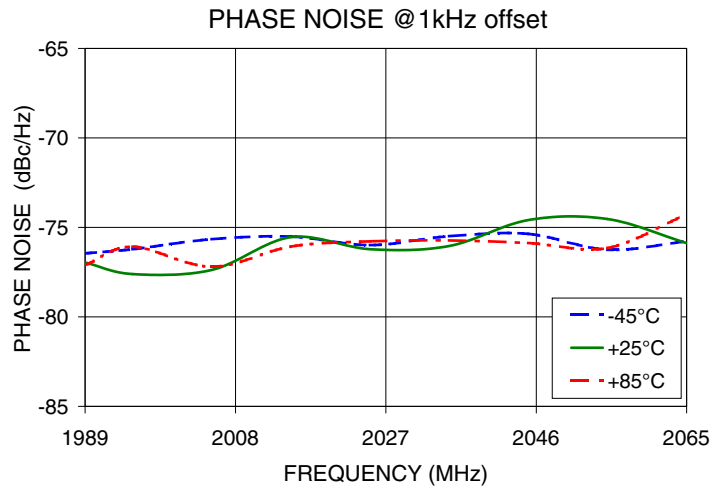
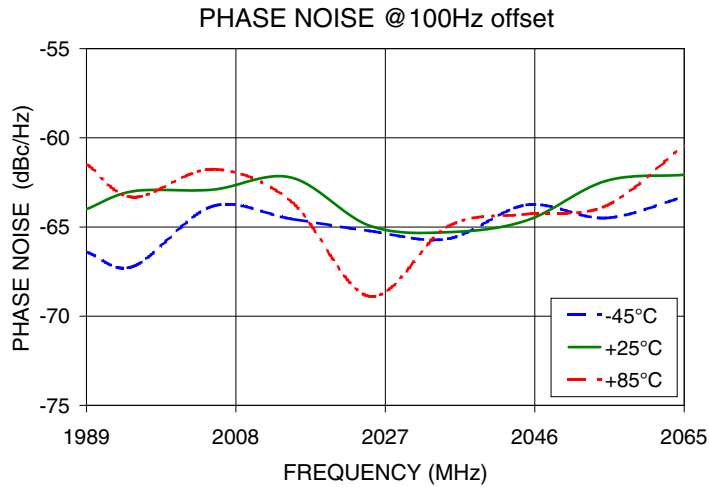
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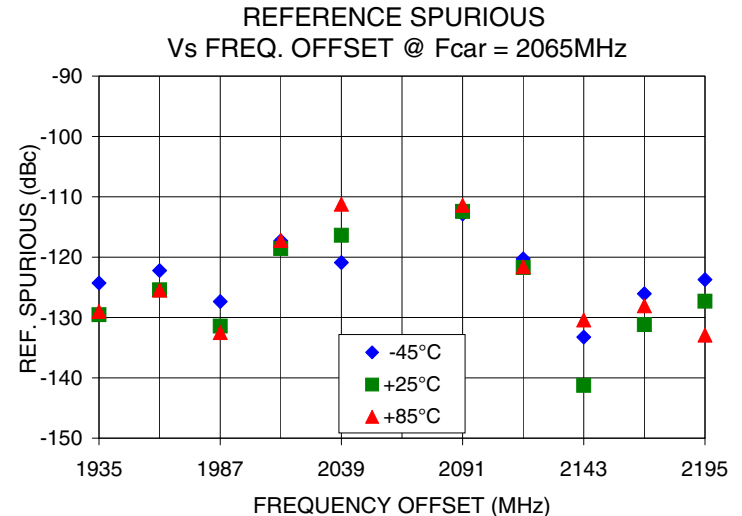
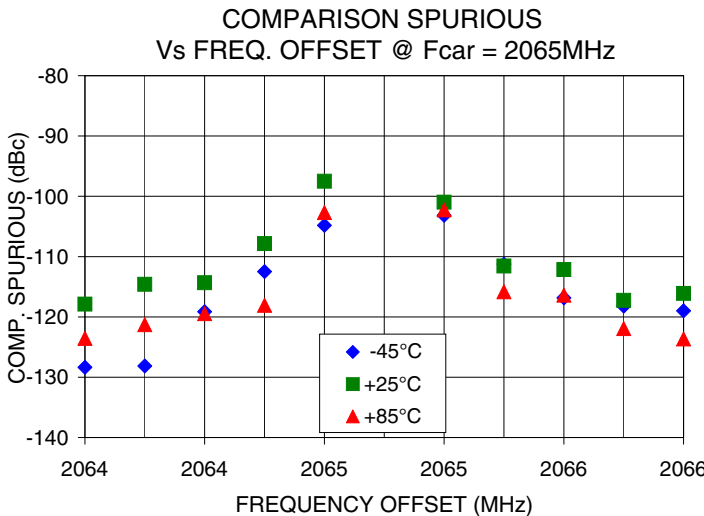
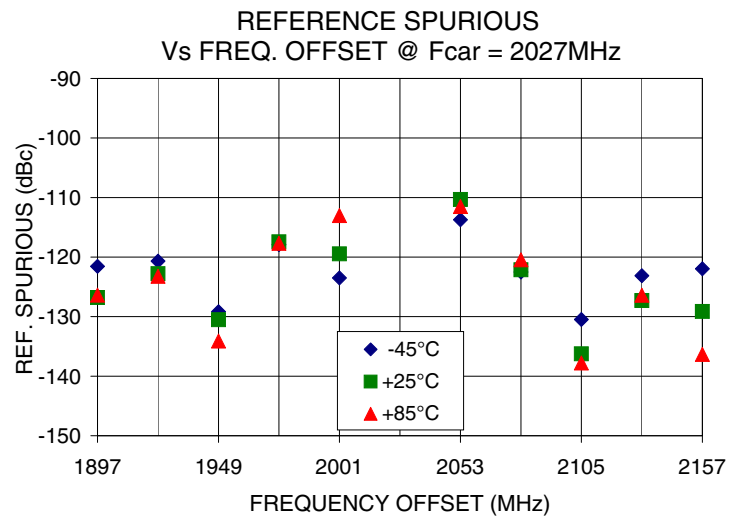
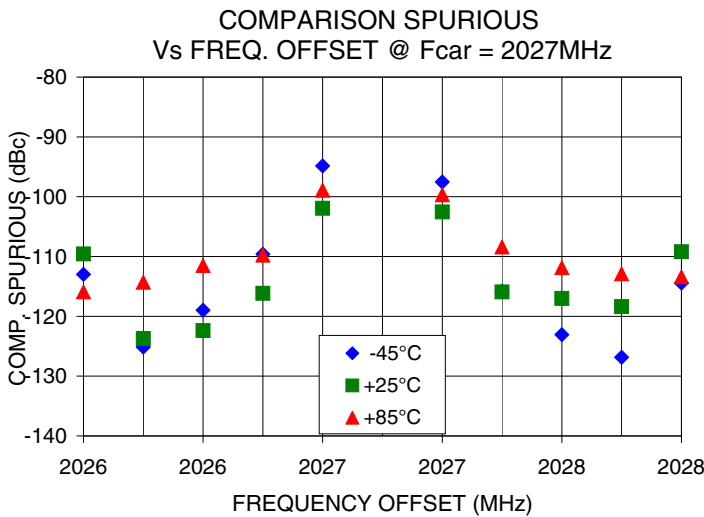
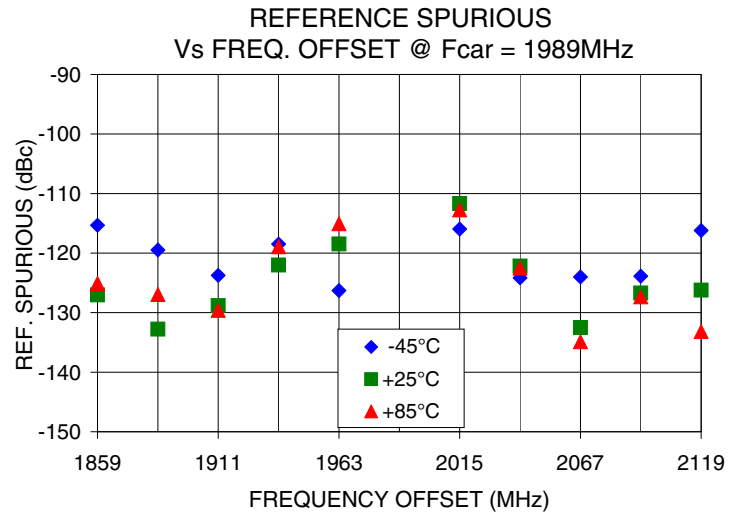
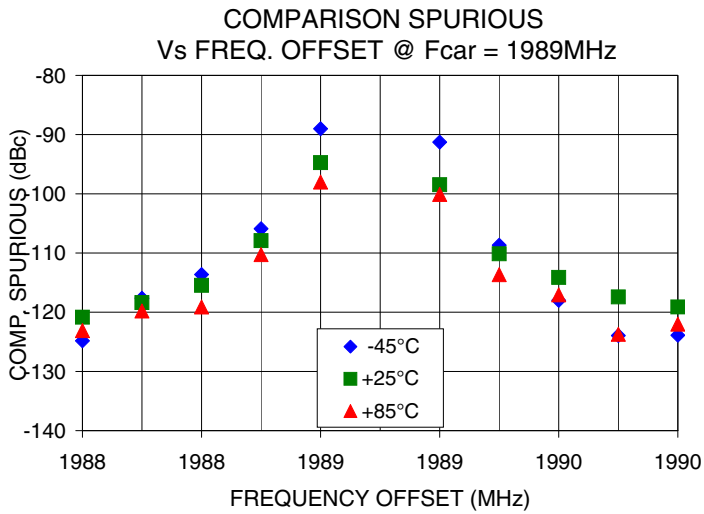
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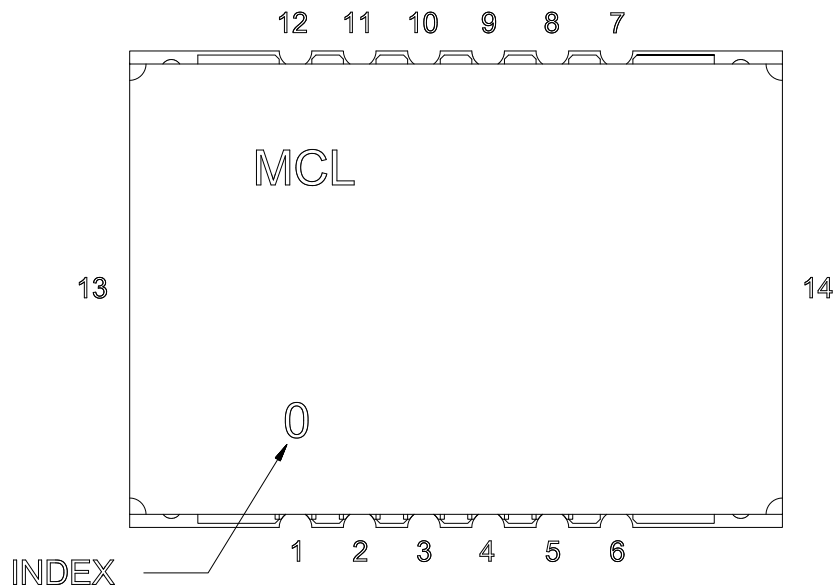


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Pin Configuration

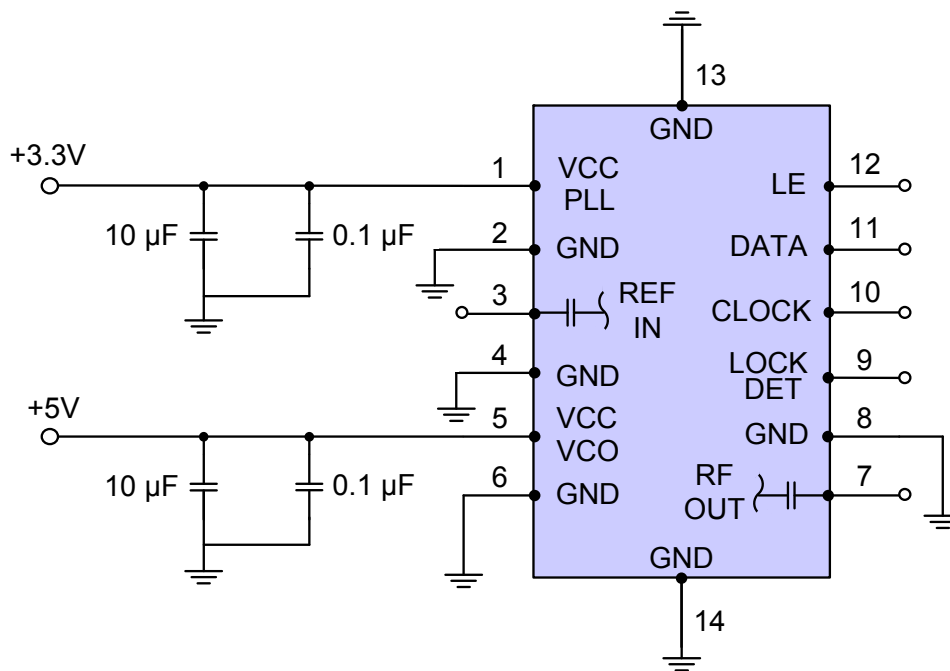


Pin Connection

Pin Number	Function
1	VCC PLL
2	GND
3	REF IN
4	GND
5	VCC VCO
6	GND
7	RF OUT
8	GND
9	LOCK DET
10	CLOCK
11	DATA
12	LE
13	GND
14	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.



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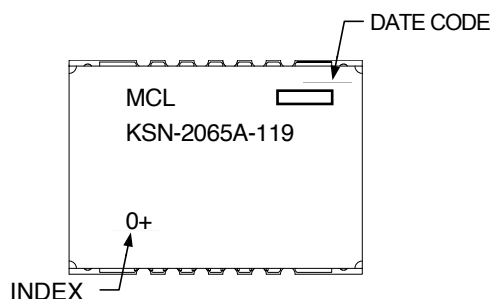


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Device Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: DK1042

Tape & Reel: TR-F28

Suggested Layout for PCB Design: PL-249

Evaluation Board: TB-567-1+

Environment Ratings: ENV03T2



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